

- 1 1. A method comprising:
2 running at least two applications; and
3 enabling the applications to share a class.
- 1 2. The method of claim 1 including enabling each
2 application to use a shared memory.
- 1 3. The method of claim 2 including enabling each
2 application to define an address space of said shared
3 memory specific to each application.
- 1 4. The method of claim 1 including duplicating
2 process specific data for each application.
- 1 5. The method of claim 4 including automatically
2 duplicating process specific data in the address space
3 specific to each application.
- 1 6. The method of claim 1 including defining a share
2 class and using the share class to execute an instance of a
3 class to share.
- 1 7. The method of claim 6 including invoking a
2 sharable interface of the class to obtain a handle.

1 8. The method of claim 7 including specifying the
2 handle on each method call to resolve the context of the
3 handle.

1 9. An article comprising a medium storing
2 instructions that enable a processor-based system to:
3 run at least two applications, and
4 enable each application to share the same class.

1 10. The article of claim 9 further storing
2 instructions that enable a processor-based system to enable
3 each application to use a shared memory.

1 11. The article of claim 10 further storing
2 instructions that enable each application to define an
3 address space of said shared memory specific to each
4 application.

1 12. The article of claim 9 further storing
2 instructions that enable the processor-based system to
3 duplicate process specific data for each application.

1 13. The article of claim 12 further storing
2 instructions that enable the processor-based system to
3 automatically duplicate process specific data in the
4 address space specific to each application.

1 14. The article of claim 9 further storing
2 instructions that enable a processor-based system to define
3 a share class and to use the share class to execute an
4 instance of the class to share.

1 15. The article of claim 14 further storing
2 instructions that enable the processor-based system to
3 invoke a shareable interface of the class to obtain a
4 handle.

1 16. The article of claim 15 further storing
2 instructions that enable the processor-based system to
3 specify the handle on each method called to resolve the
4 context of the handle.

1 17. A system comprising:
2 a processor; and
3 a storage coupled to said processor, said storage
4 storing instructions that enable the processor to run at
5 least two applications and enable each application to share
6 the same class.

1 18. The system of claim 17 wherein said storage
2 stores instructions that enable the processor to enable
3 each application to use a shared memory.

